

HBU2 tile map (top view)



BOTTOM

5	4	3 _S	2	1	0	5	4	3	2	1	0 _{T2}	
11 _S	10	9	8	7	6	11	10	9	8	7	6	
17	16	15	14	13	12	17	16	15	14	13	12	
		SPIROC2_4						SPIROC2_3		T1		
23 _S	22	21 _S	20	19	18 _S	23	22	21	20	19	18	
29	28	27	26	25	24	29	28	27	26	25	24	
35	34	33	32	31	30	35	34	33	32	31	30	
	T3											
0	1	2	3	4	5 _K	0	1 _K	2 _K	3 _K	4 _K	5 _K	
					T5							
6 _K	7	8	9	10	11	6	7	8	9	10 _K	11 _K	
12	13	14	15	16	17	12	13	14	15	16 _K	17 _K	
		SPIROC2_2						SPIROC2_1				
18	19	20	21 _K	22	23	18	19	20	21	22	23	
24	25	26	27	28	29	24	25	26	27	28	29	
30 _{T6}	31 _K	32	33	34	35	T4	30 _K	31	32	33	34 _K	35

Signal In

TOP

Power In

K = 12k KETEK, ° soldered 2 times

S = SENSL

SP2-1: with filter for preamp,
all memcells ok.